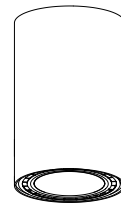
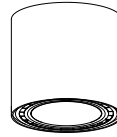


x 2



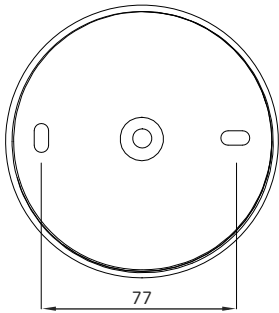
x 1



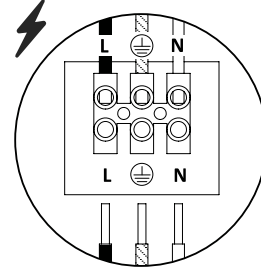
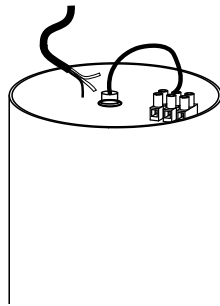
BIT S
9482 | 9486

BIT M
9481 | 9485

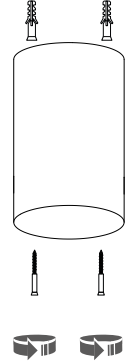
1



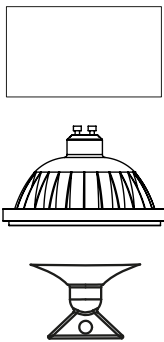
2



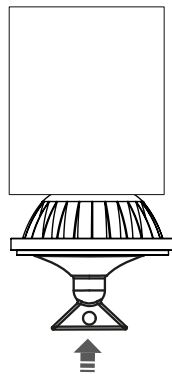
3



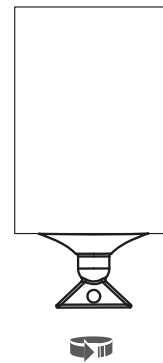
4



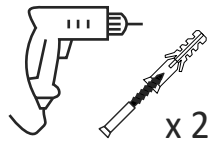
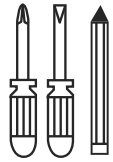
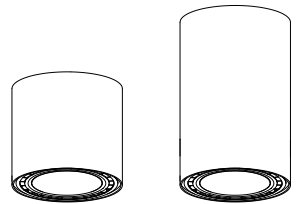
5



6



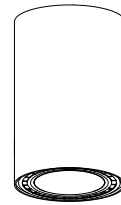
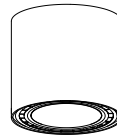
7



x 2



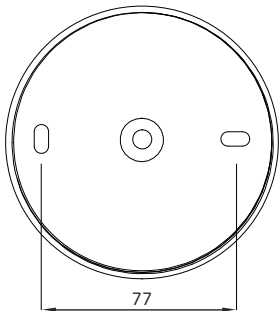
x 1



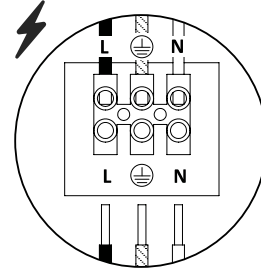
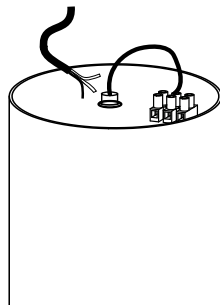
BIT S
9482 | 9486

BIT M
9481 | 9485

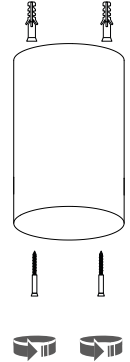
1



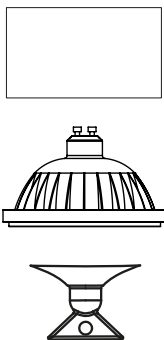
2



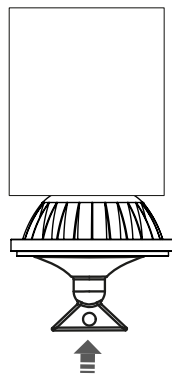
3



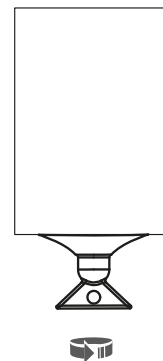
4



5



6



7

